

**In the Specification**

Please substitute the following amended paragraph for the paragraph 0033:

[033] It should be appreciated that there are a variety of ways to implement the requisite circuitry and logic for carrying out the functionality and operations described above. One such particular implementation is described and illustrated and illustrated in co-pending application serial number 10/630,460 ~~xx/yy,zzz~~, filed on the same day as the present application (and entitled "Integrated circuit with a scalable high-bandwidth architecture" ~~identified by attorney docket number 50833-1240~~), which co-pending application is incorporated herein by reference.